

## What is claimed is:

	1	<b>\</b> J.	An interface system suitable for coupling a first bus interface controller with a
W	$\frac{1}{\sqrt{2}}$	secon	nd bus interface controller, comprising:
that the time that that that the time the time the time that	VI		a first bus interface controller; and
	/ l 4		a second bus interface controller wherein the second bus interface controller is
	5		coupled to the first bus interface controller via an interface including
	6		a command queuing interface suitable for enqueueing a
	7		transaction;
	8		a command completion interface suitable for reporting transaction
	9		completion; and
	10		a data transfer interface suitable for transferring data.
	10		
	1	2.	The interface system as described in claim 1, wherein command and control
	1	2.	information are suitable for being exchanged on at least one of the command
	2		1
	3		queuing interface and command completion interface while data is exchanged or
	4		the data transfer interface
	1	3.	The interface system as described in claim 1, wherein data for a transaction is
	2		suitable for being moved without respect to a current transaction being requested
	3		on a control bus.
	1	4.	The interface system as described in claim 1, wherein a backend master device
	2		enqueues a transaction on the command queuing interface, at least one transfer
	3		of data is accomplished corresponding to the transaction queued on the
	4		command queuing interface, and completion status of the transaction is reported

on the command completion interface.

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- The interface system as described in claim 1, wherein a plurality of transactions are queued, the transaction are completed without regard to an order the transactions are queued.
- The interface system as described in claim 1, wherein the first bus interface controller is suitable for coupling to a backend device and the second bus interface controller is suitable for coupling to an internal bus of an information handling system.
- The interface system as described in claim 1, wherein the first bus interface controller conforms to at least one of a USB standard, SCSI standard, fiber standard and the second bus interface conforms to at least one of a PCI standard and PCI-X standard.
  - 8. The interface system as described in claim 1, wherein a plurality of data transfers on the data transfer interface are executed, the plurality of data transfers corresponding to a transaction queued on the command queuing interface.

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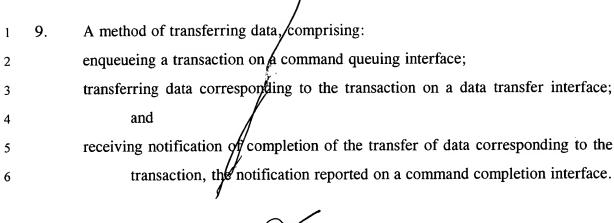
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The method as described in daim 9, wherein a plurality of transactions are queued, the transaction are completed without regard to an order the transactions are queued.

The method as described in claim 9, wherein command and control information are suitable for being exchanged on at least one of the command queuing interface and command completion interface while data is exchanged on the data transfer interface.

The method as described in claim 9, wherein data for a transaction is suitable for being moved without respect to a current transaction being requested on a control bus.

The method as described in claim 9, wherein a backend master device enqueues a transaction on the command queuing interface, at least one transfer of data is accomplished corresponding to the transaction queued on the command queuing interface, and completion status of the transaction is reported on the command completion interface.

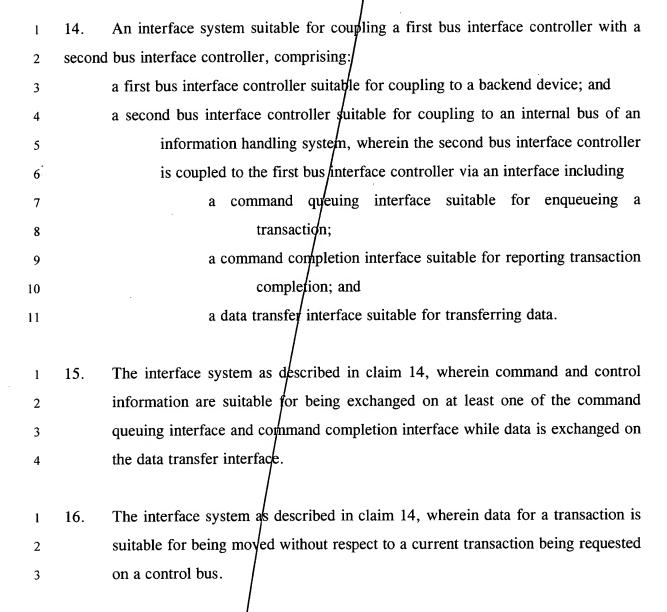
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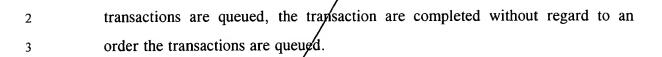
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- 17. The interface system as described in claim 14, wherein a backend master device enqueues a transaction on the command queuing interface, at least one transfer of data is accomplished corresponding to the transaction queued on the command queuing interface, and completion status of the transaction is reported on the command completion interface.
- 18. The interface system as described in claim 14, wherein a plurality of

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- The interface system as described in claim 14, wherein the first bus interface controller conforms to at least one of a USB standard, SCSI standard, fiber standard and the second bus interface conforms to at least one of a PCI standard and PCI-X standard.
- The interface system as described in claim 14, wherein a plurality of data transfers on the data transfer interface are executed, the plurality of data transfers corresponding to a transaction queued on the command queuing interface.

